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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/522,845	01/28/2005	Robert Kofler	A T02 0047 US	9348
24738	7590 10/02/2006		EXAM	INER
	LECTRONICS NORT	TRIEU, VAN THANH		
	INTELLECTUAL PROPERTY & STANDARDS 1109 MCKAY DRIVE, M/S-41SJ SAN JOSE, CA 95131		ART UNIT	PAPER NUMBER
SAN JOSE,			2612	

DATE MAILED: 10/02/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)					
Office Action Commence	10/522,845	KOFLER, ROBERT					
Office Action Summary	Examiner	Art Unit					
	Van T. Trieu	2612					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1)⊠ Responsive to communication(s) filed on 28 Ja	nuary 2005.						
2a) ☐ This action is <b>FINAL</b> . 2b) ☒ This	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.						
	- ''						
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims							
4)⊠ Claim(s) <u>1-4</u> is/are pending in the application.							
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1-4</u> is/are rejected.							
· ·	7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.							
Application Papers							
9) The specification is objected to by the Examiner.							
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under 35 U.S.C. § 119							
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:							
<ol> <li>Certified copies of the priority documents have been received.</li> </ol>							
2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date							
3) Information Disclosure Statement(s) (PTO/SB/08)  5) Notice of Informal Patent Application							
Paper No(s)/Mail Date <u>1/28/05</u> .	6)						

#### **DETAILED ACTION**

## Specification

The following guidelines illustrate the preferred layout for the specification of a utility application. These guidelines are suggested for the applicant's use.

### Arrangement of the Specification

As provided in 37 CFR 1.77(b), the specification of a utility application should include the following sections in order. Each of the lettered items should appear in upper case, without underlining or bold type, as a section heading. If no text follows the section heading, the phrase "Not Applicable" should follow the section heading:

- (a) TITLE OF THE INVENTION.
- (b) CROSS-REFERENCE TO RELATED APPLICATIONS.
- (c) STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR DEVELOPMENT.
- (d) THE NAMES OF THE PARTIES TO A JOINT RESEARCH AGREEMENT.
- (e) INCORPORATION-BY-REFERENCE OF MATERIAL SUBMITTED ON A COMPACT DISC.
- (f) BACKGROUND OF THE INVENTION.
  - (1) Field of the Invention.
  - (2) Description of Related Art including information disclosed under 37 CFR 1.97 and 1.98.
- (g) BRIEF SUMMARY OF THE INVENTION.
- (h) BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING(S).
- (i) DETAILED DESCRIPTION OF THE INVENTION.
- (i) CLAIM OR CLAIMS (commencing on a separate sheet).
- (k) ABSTRACT OF THE DISCLOSURE (commencing on a separate sheet).
- The disclosure is objected to because of the following informalities: the

Specification filed on 28 January 2005 is not arranged with the section headings such as the section headings (b), (f), (g), (h) and (i) listed above.

Appropriate correction is required.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-4 are rejected under 35 U.S.C. 102(b) as being anticipated by **Koller et al** [US 6,304,613].

Regarding claim 1, the claimed a transponder that is arranged for non-contacting communication with a communication station and that has transmission means and that has an integrated circuit with circuit connecting contacts, wherein the transmission means are connected to the circuit connecting contacts and an input voltage can be picked off from the circuit connecting contacts (the data carrier transponder 1 having transmission coil 3 having an amplitude-modulated carrier signal TS connected to first and second rectifier stages 6 and 7 to provide or picked up dc voltage V from the carrier TS signal to the IC data processing means 15 and other electronic circuits 12, 13, 16 and 18, see Figs. 1, 6 and 7, col. 1, lines 46-52, col. 4, lines 33-50, col. 5, lines 65-67 and col. 6, lines 1-6);

wherein the integrated circuit has a first circuit section and a second circuit section, wherein the first circuit section is arranged for being supplied with a first supply voltage (the first set of circuits 6 and 9 generates first voltage V within a limit voltage value UL1 or U1, see Figs. 1 and 2, col. 4, lines 61-67, col. 5, lines 1-9, col. 6, lines 7-21 and col. 7, lines 65-66); and the second circuit section is arranged for being supplied

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with a second supply voltage (the second set of circuits 7 and 25 generates a second voltage V within a limit voltage value UL2 or U2, see Figs. 1 and 2, col. 6, lines 23-31, col. 7, lines 10-36 and 66-67);

wherein first rectifier means and limiter means cooperating with the first rectifier means are provided, wherein a voltage representing the input voltage can be fed to the first rectifier means, wherein the first supply voltage can be picked off from the first rectifier means or from the limiter means (the first rectifier stage 6 and first limiter means 9 generates voltage V picked off from the first rectifier stage 6 or limiting voltage value UL1 or U1, see Figs. 1-5, col. 5, lines 1-9 and col. 6, lines 7-21);

wherein second rectifier means and control means for controlling the second rectifier means are provided, wherein a voltage representing the input voltage can also be fed to the second rectifier means, wherein the second supply voltage can be picked off from the second rectifier means, wherein the value of the second supply voltage that can be picked off from the second rectifier means can be controlled by the control means (the control second signal control generating means 29 generates a second control signal S2 corresponding to the second rectifier carrier signal and applied without delay to the second limiter means 25 to provide voltage V picked off from the second rectifier stage 7 or limiting voltage value UL1 or U1, see Figs. 1-5, col. 6, lines 22-30 and col. 7, lines 10-36).

Regarding claim 2, the claimed control means are arranged to control the value of the second supply voltage as a function of the value of the output variable arising at the

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output of the second rectifier means (the control second signal control generating means 29 generates a second control signal S2 corresponding to the second rectifier carrier signal, see Figs. 1-5, col. 6, lines 22-30 and col. 7, lines 10-36).

Regarding claim 3, all the claimed subject matters are cited in respect to claim 1 above.

Regarding claim 4, all the claimed subject matters are cited in respect to claims 2 and 3 above.

#### Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Masui discloses a semiconductor IC RFID transponder and non-contact IC card comprising a transmission coil, a rectifier circuit for converting an alternating current into a direct-current voltage and outputting it as a power supply voltage. [US 6,809,952]

Lee discloses an RFID transponder using a RF powered voltage pump to generate a high voltage to erase and to write the EEPROM. [US 6,515,919]

Mitzoguchi discloses a semiconductor IC constructed with multiple stages of circuit blocks connected in vertical series between a first power supply line and a second power supply line. [US 6,031,413]

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4. Any inquiry concerning this communication or earlier communications from examiner should be directed to primary examiner **Van Trieu** whose telephone number is (571) 272-2972. The examiner can normally be reached on Mon-Fri from 7:00 AM to 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Mr. Mike Horabik** can be reached on (571) 272-3068.

Van Trieu

**Primary Examiner** 

Date: 9/21/06